

<b>Notice of References Cited</b>	Application/Control No. 10/634,437	Applicant(s)/Patent Under Reexamination LIU ET AL.	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, p. 184.
	V	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, pp. 368-369.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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